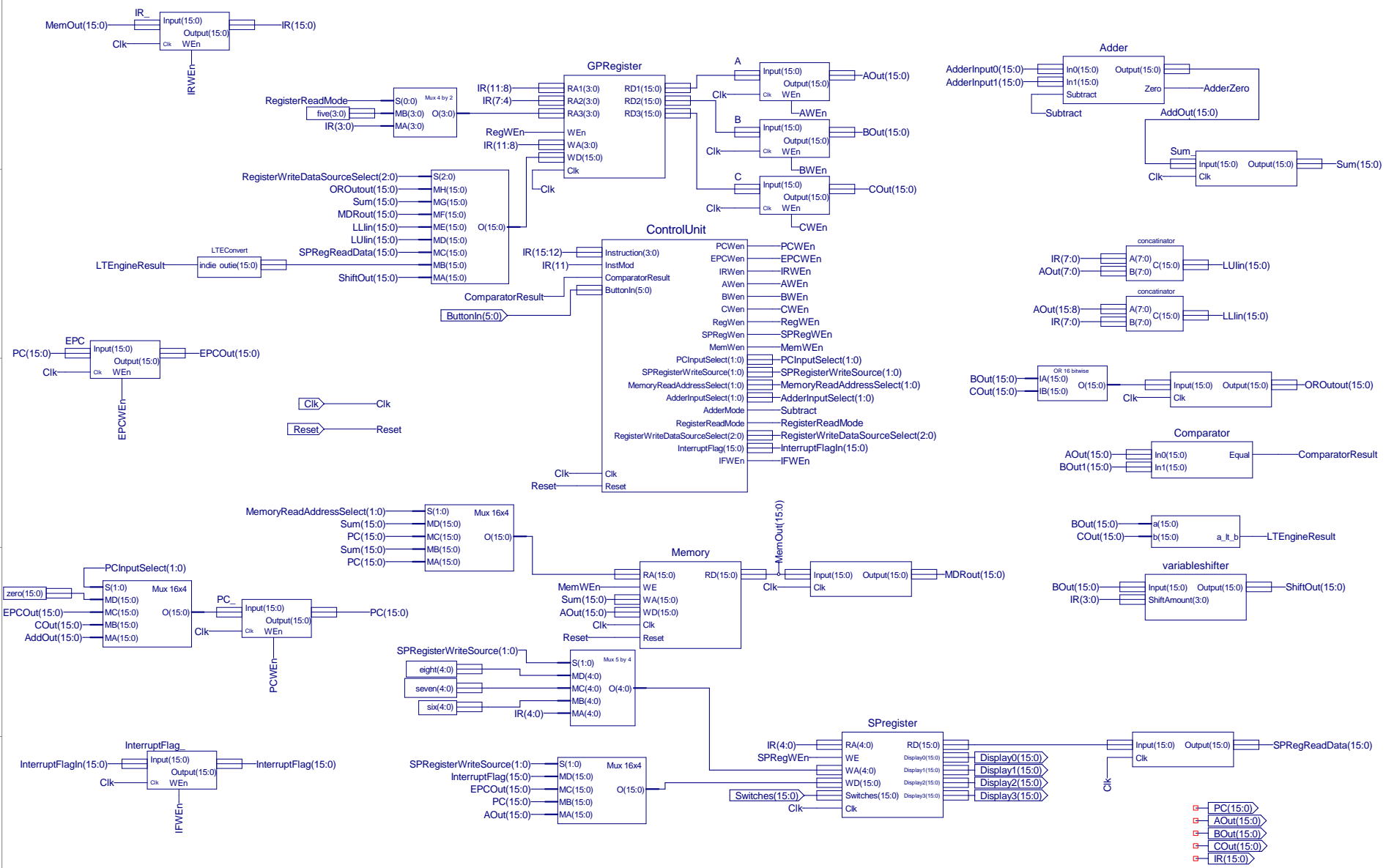


# Mach 5

## Processor Architecture

1-04 [Buita, Kaffenberger, LaRocca, Mariani]



- PC(15:0)
- AOut(15:0)
- BOut(15:0)
- COut(15:0)
- IR(15:0)