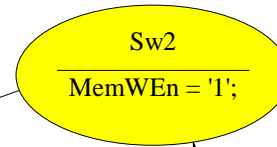
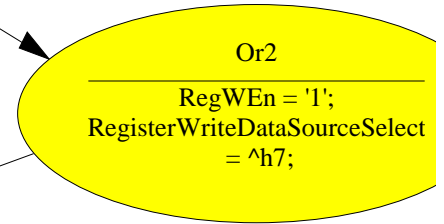
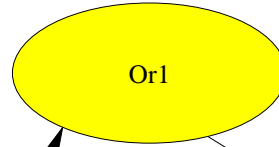
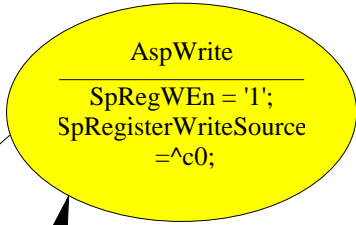
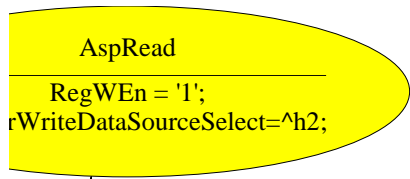


Register B0

Instruction3:
Instruction2 :
Instruction1:
Instruction0 :
InstMoc

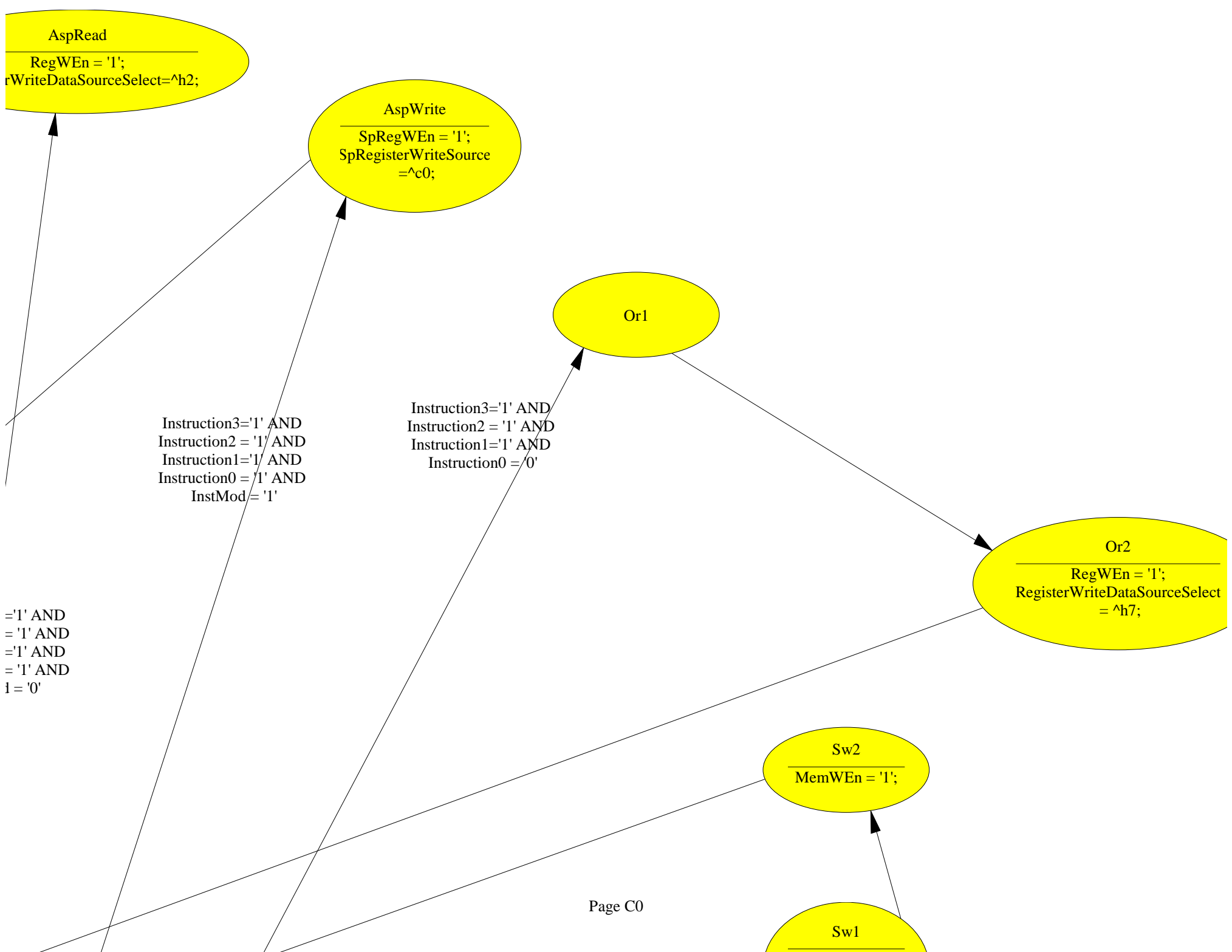


= '1' AND
= '1' AND
= '1' AND
= '1' AND
1 = '0'

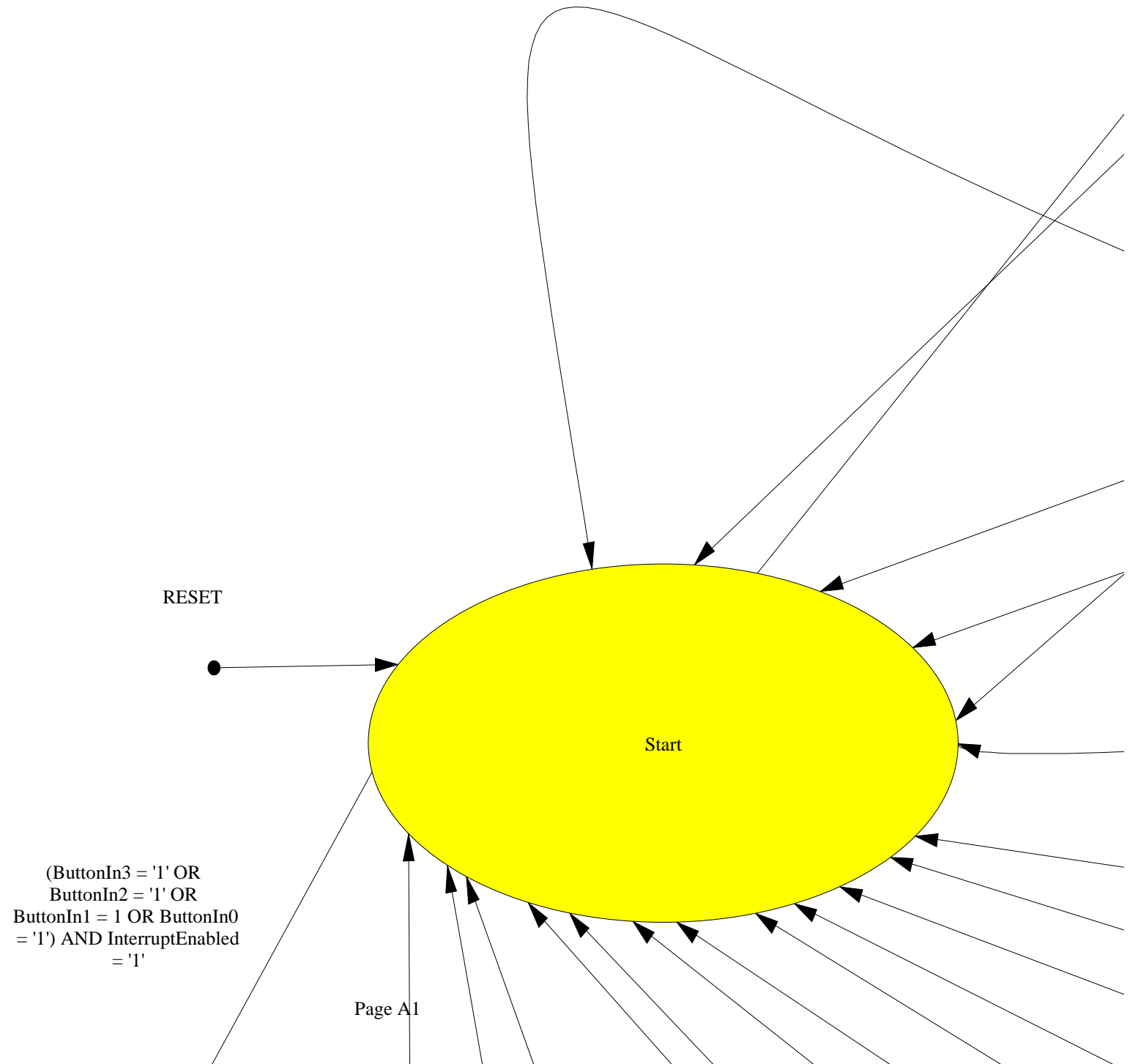
Instruction3 = '1' AND
Instruction2 = '1' AND
Instruction1 = '1' AND
Instruction0 = '1' AND
InstMod = '1'

Instruction3 = '1' AND
Instruction2 = '1' AND
Instruction1 = '1' AND
Instruction0 = '0'

Page C0





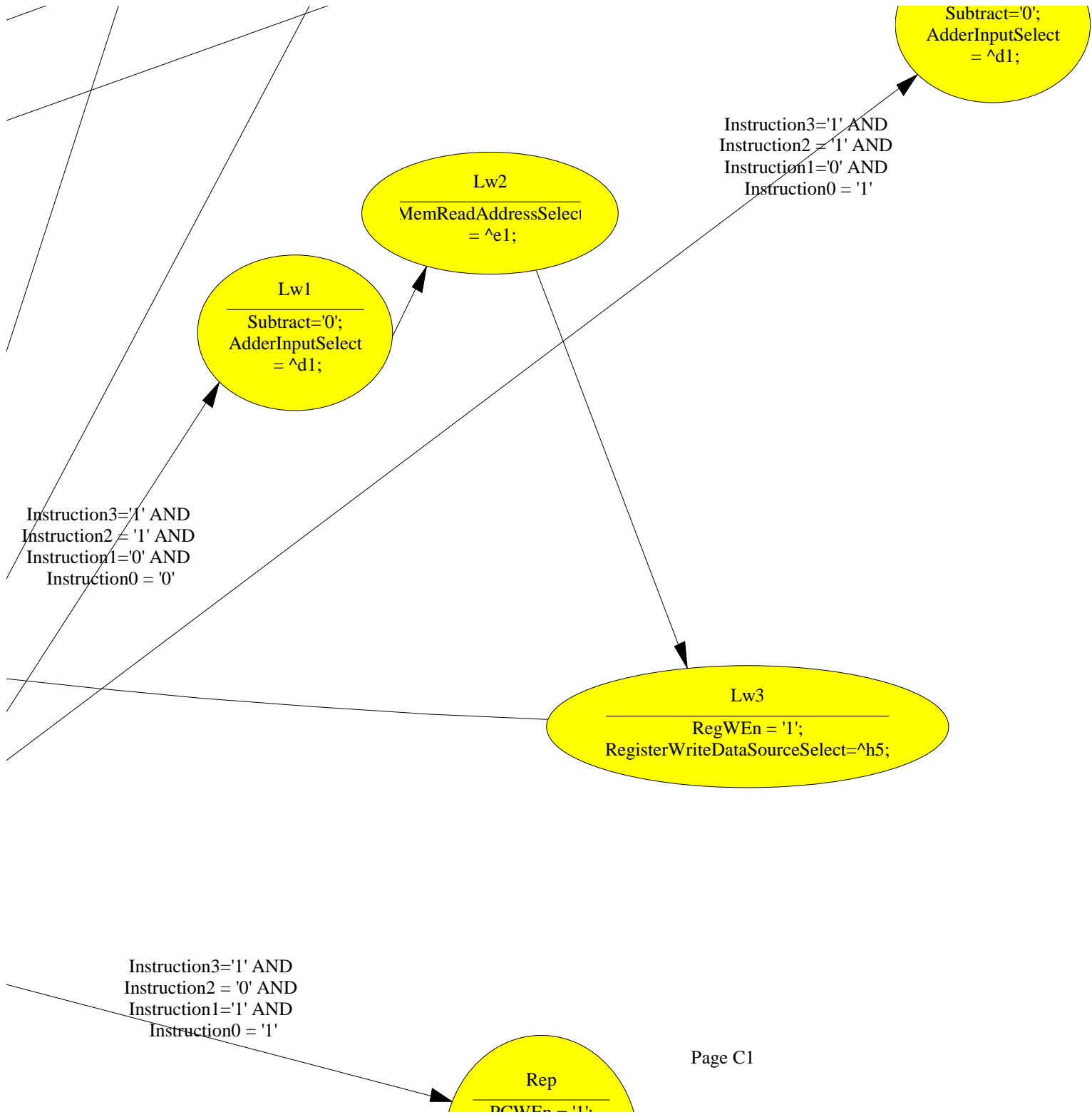


AdderInputSelect = ^d0;
Subtract = '0';
RegisterReadMode = '0';
RegisterWriteDataSourceSelect = ^f0;
InterruptFlag = ^g0;

@else

Decode

EPCWEn = '0';
PCWEn = '0';
SPRegWEn = '1';
AWEn = '1';
BWEn = '1';
CWEn = '1';
SPRegisterWriteSource = ^c1;
RegisterReadMode = '0';





Instruction3='0' AND
Instruction2 = '0' AND
Instruction1='1' AND
Instruction0 = '0'

Instruction3='0' AND
Instruction2 = '0' AND
Instruction1='0' AND
Instruction0 = '1'

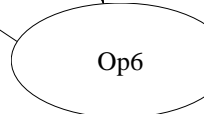
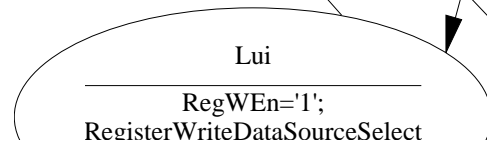
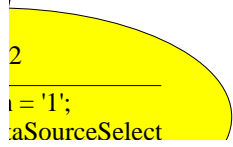
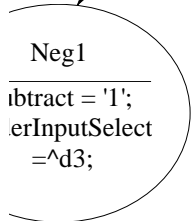
Instruction3='0' AND
Instruction2 = '1' AND
Instruction1='1' AND
Instruction0 = '0'

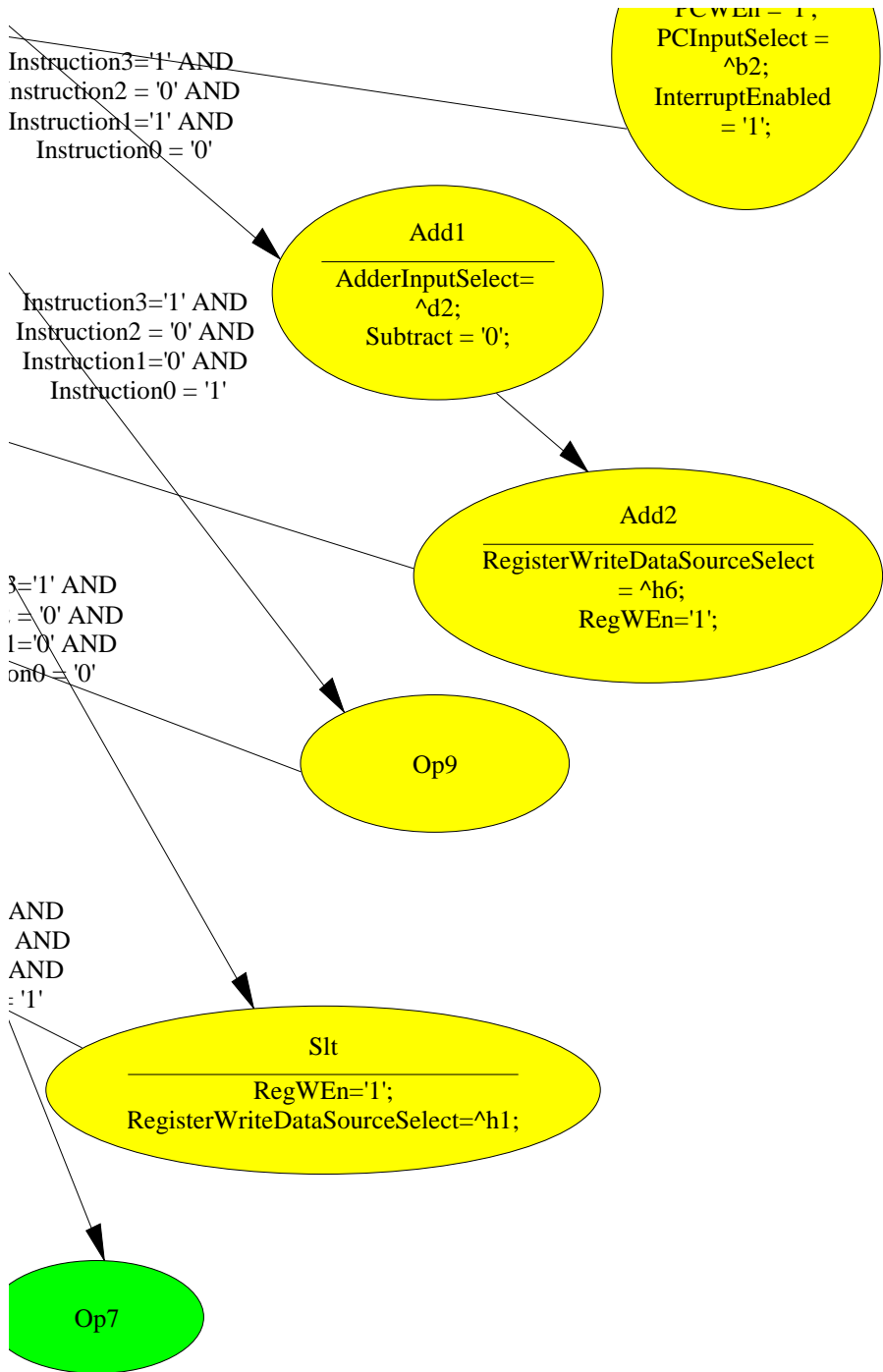
Instruction3='0' AND
Instruction2 = '1' AND
Instruction1='1' AND
Instruction0 = '1'

Instruction3='0' AND
Instruction2 = '1' AND
Instruction1='0' AND
Instruction0 = '0'

Instruction3='0' AND
Instruction2 = '1' AND
Instruction1='0' AND
Instruction0 = '1'

Instruction3='0' AND
Instruction2 = '1' AND
Instruction1='1' AND
Instruction0 = '1'





$\epsilon = \hat{h}$

6;

= ^h3;

RegWEn = '1';
RegisterWriteDataSourceSelect
= ^h0;

Lli

RegWEn='1';
RegisterWriteDataSourceSelect=^h4;

